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จัดพิมพ์โดยหน่วยซ่อมสร้างเครื่องอิเล็กทรอนิกส์ ภาควิชาสรีรวิทยา คณะแพทยศาสตร์  
ศิริราชพยาบาล มหาวิทยาลัยมหิดล กรุงเทพมหานคร, พ.ศ.๒๕๒๔.
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# Operational Amplifiers/Buffers

## LM4250/LM4250C programmable operational amplifier

### general description

The LM4250 and LM4250C are extremely versatile programmable monolithic operational amplifiers. A single external master bias current setting resistor programs the input bias current, input offset current, quiescent power consumption, slew rate, input noise, and the gain-bandwidth product. The device is a truly general purpose operational amplifier.

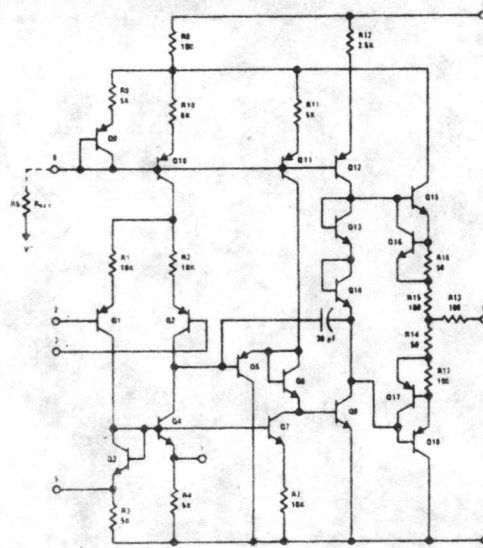
- Standby power consumption as low as 500 nW
- No frequency compensation required
- Programmable electrical characteristics
- Offset Voltage nulling capability
- Can be powered by two flashlight batteries
- Short circuit protection

### features

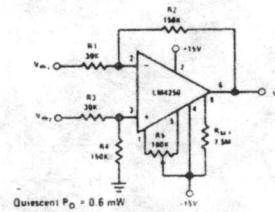
- $\pm 4V$  to  $\pm 18V$  power supply operation
- 3 nA input offset current

The LM4250C is identical to the LM4250 except that the LM4250C has its performance guaranteed over a  $0^{\circ}C$  to  $70^{\circ}C$  temperature range instead of the  $-55^{\circ}C$  to  $+125^{\circ}C$  temperature range of the LM4250.

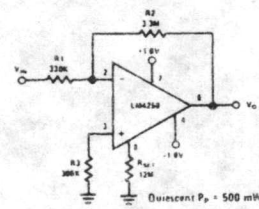
### schematic diagrams



### typical applications



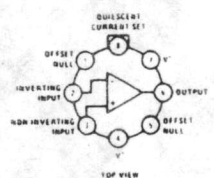
X5 Difference Amplifier



500 Nano-Watt X10 Amplifier

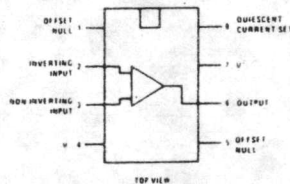
### connection diagrams

#### Metal Can Package



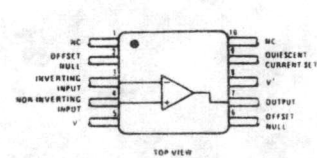
Order Number LM4250H or LM4250CH  
See Package 11

#### Dual-In-Line Package



Order Number LM4250CN  
See Package 20  
Order Number LM4250J  
or LM4250CJ  
See Package 15

#### Flat Package



Order Number LM4250F  
See Package 3

LM4250/LM4250C

**absolute maximum ratings**

Supply Voltage	±18V	Output Short-Circuit Duration	Indefinite
Power Dissipation (Note 1)	500 mW	Operating Temperature Range	LM4250 -55°C ≤ T <sub>A</sub> ≤ 125°C
Differential Input Voltage	±30V		LM4250C 0°C ≤ T <sub>A</sub> ≤ 70°C
Input Voltage (Note 2)	±15V	Storage Temperature Range	-65°C to 150°C
I <sub>SET</sub> Current	150 μA	Lead Temperature (Soldering, 10 sec)	300°C

**electrical characteristics** LM4250 (-55°C ≤ T<sub>A</sub> ≤ 125°C unless otherwise specified)

PARAMETERS	CONDITIONS	V <sub>S</sub> = ±1.5V			
		I <sub>SET</sub> = 1 μA		I <sub>SET</sub> = 10 μA	
		MIN	MAX	MIN	MAX
V <sub>OS</sub>	T <sub>A</sub> = 25° R <sub>S</sub> ≤ 100 kΩ		3 mV		5 mV
I <sub>OS</sub>	T <sub>A</sub> = 25°		3 nA		10 nA
I <sub>bias</sub>	T <sub>A</sub> = 25°		7.5 nA		50 nA
Large Signal Voltage Gain	T <sub>A</sub> = 25° R <sub>L</sub> = 100 kΩ V <sub>O</sub> = ±0.6, R <sub>L</sub> = 10 kΩ	40k		50k	
Supply Current	T <sub>A</sub> = 25°C		7.5 μA		80 μA
Power Consumption	T <sub>A</sub> = 25°C		23 μW		240 μW
V <sub>OS</sub>	R <sub>S</sub> ≤ 100 kΩ		4 mV		6 mV
I <sub>OS</sub>	T <sub>A</sub> = 125°C		5 nA		10 nA
I <sub>bias</sub>	T <sub>A</sub> = -55°C		3 nA		10 nA
I <sub>bias</sub>			7.5 nA		50 nA
Input Voltage Range		±0.7V		±0.7V	
Large Signal Voltage Gain	V <sub>O</sub> = ±0.6V R <sub>L</sub> = 100 kΩ	30k		30k	
Output Voltage Swing	R <sub>L</sub> = 10 kΩ R <sub>L</sub> = 100 kΩ	±0.6V		±0.6V	
Common Mode Rejection Ratio	R <sub>S</sub> ≤ 10 kΩ	70 dB		70 dB	
Supply Voltage Rejection Ratio	R <sub>S</sub> ≤ 10 kΩ	76 dB		76 dB	
Supply Current			8 μA		90 μA
Power Consumption			24 μW		270 μW

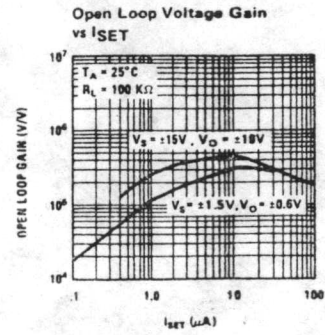
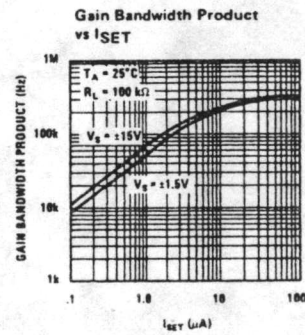
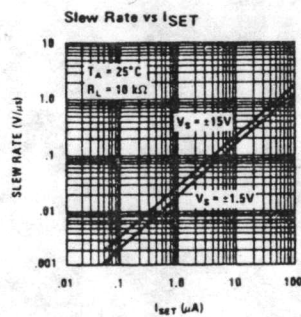
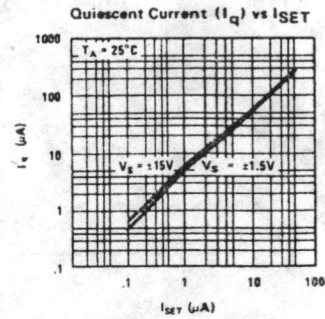
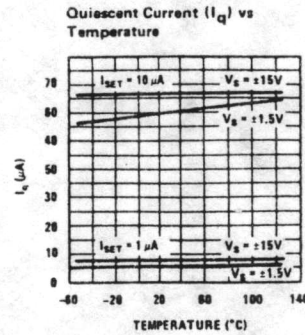
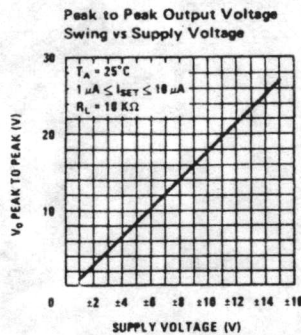
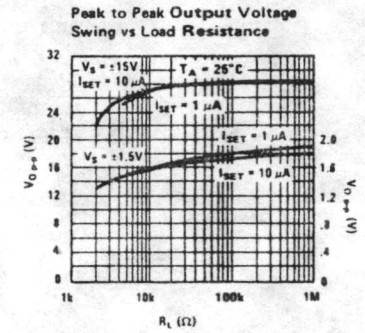
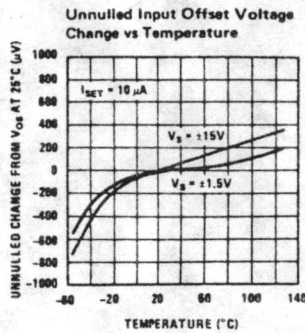
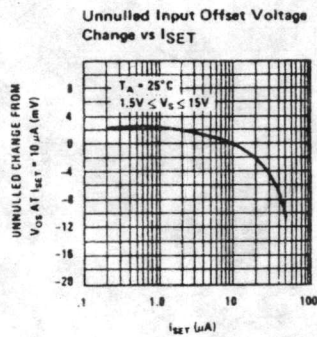
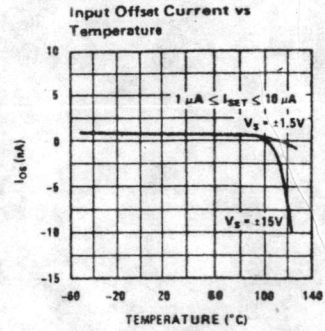
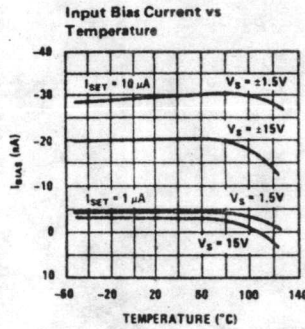
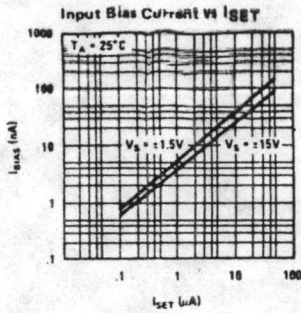
  

PARAMETERS	CONDITIONS	V <sub>S</sub> = ±15V			
		I <sub>SET</sub> = 1 μA		I <sub>SET</sub> = 10 μA	
		MIN	MAX	MIN	MAX
V <sub>OS</sub>	T <sub>A</sub> = 25°C R <sub>S</sub> ≤ 100 kΩ		3 mV		5 mV
I <sub>OS</sub>	T <sub>A</sub> = 25°C		3 nA		10 nA
I <sub>bias</sub>	T <sub>A</sub> = 25°C		7.5 nA		50 nA
Large Signal Voltage Gain	T <sub>A</sub> = 25°C R <sub>L</sub> = 100 kΩ V <sub>O</sub> = ±10V R <sub>L</sub> = 10 kΩ	100k		100k	
Supply Current	T <sub>A</sub> = 25°C		10 μA		90 μA
Power Consumption	T <sub>A</sub> = 25°C		300 μW		2.7 mW
V <sub>OS</sub>	R <sub>S</sub> ≤ 100 kΩ		4 mV		6 mV
I <sub>OS</sub>	T <sub>A</sub> = 125°C		25 nA		25 nA
I <sub>bias</sub>	T <sub>A</sub> = -55°C		3 nA		10 nA
I <sub>bias</sub>			7.5 nA		50 nA
Input Voltage Range		±13.5V		±13.5V	
Large Signal Voltage Gain	V <sub>O</sub> = ±10V R <sub>L</sub> = 100 kΩ R <sub>L</sub> = 10 kΩ	50k		50k	
Output Voltage Swing	R <sub>L</sub> = 100 kΩ R <sub>L</sub> = 10 kΩ	±12V		±12V	
Common Mode Rejection Ratio	R <sub>S</sub> ≤ 10 kΩ	70 dB		70 dB	
Supply Voltage Rejection Ratio	R <sub>S</sub> ≤ 10 kΩ	76 dB		76 dB	
Supply Current			11 μA		100 μA
Power Consumption			330 μW		3 mW

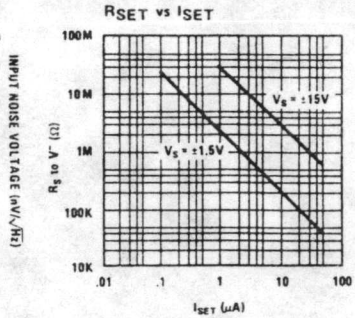
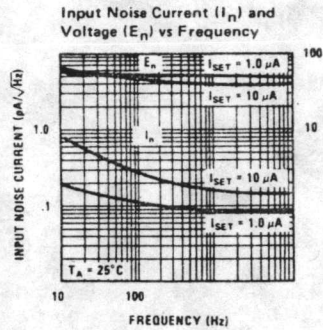
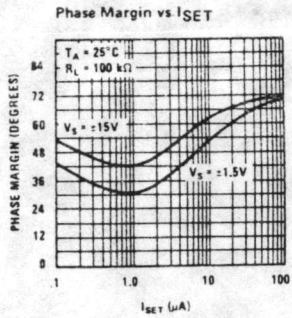
Note 1: The maximum junction temperature of the LM4250 is 150°C, while that of the LM4250C is 100°C. For operating at elevated temperatures, devices in the TO-5 package must be derated based on a thermal resistance of 150°C/W junction to ambient, or 45°C/W junction to case. The thermal resistance of the dual-in-line package is 125°C/W.

Note 2: For supply voltages less than ±15V, the absolute maximum input voltage is equal to the supply voltage.

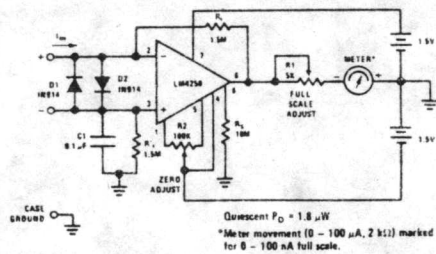
typical performance characteristics



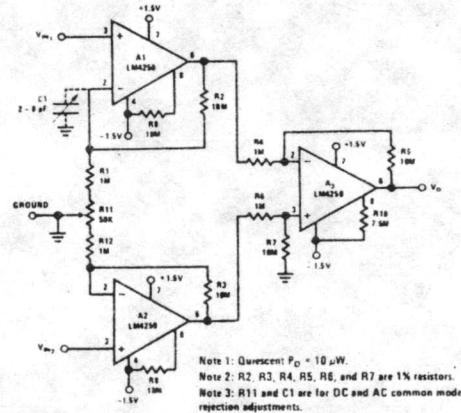
typical performance characteristics (con't)



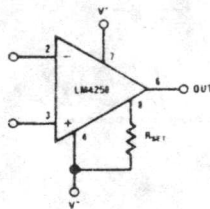
typical applications (con't)



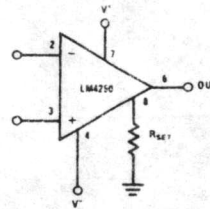
Floating Input Meter Amplifier  
100 Nano-Ampere Full Scale



X100 Instrumentation Amplifier 10 uW



RSET Connected to V<sup>-</sup>

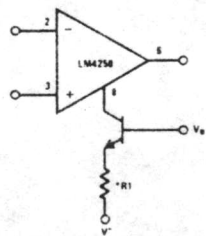


RSET Connected to Ground

ISET EQUATIONS

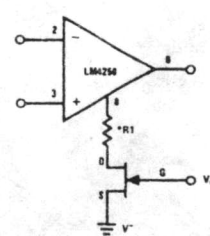
$$I_{SET} = \frac{V^+ - V^- - 0.5}{R_{SET}} \quad \text{where } R_{SET} \text{ is connected to } V^-$$

$$I_{SET} = \frac{V^+ - 0.5}{R_{SET}} \quad \text{where } R_{SET} \text{ is connected to ground.}$$

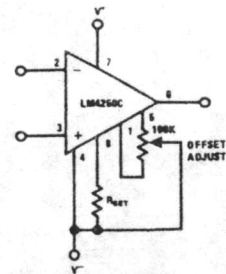


Transistor Current Source Biasing

\*R1 limits ISET maximum



FET Current Source Biasing



Offset Null Circuit





electrical characteristics LM4250C (0°C ≤ T <sub>A</sub> ≤ 70°C unless otherwise specified)					
PARAMETERS	CONDITIONS	V <sub>S</sub> = ±1.5V			
		I <sub>SET</sub> = 1 μA		I <sub>SET</sub> = 10 μA	
		MIN	MAX	MIN	MAX
V <sub>OS</sub>	T <sub>A</sub> = 25°C R <sub>S</sub> ≤ 100 kΩ		5 mV		6 mV
I <sub>OS</sub>	T <sub>A</sub> = 25°C		6 nA		20 nA
I <sub>BIAS</sub>	T <sub>A</sub> = 25°C		10 nA		75 nA
Large Signal Voltage Gain	T <sub>A</sub> = 25°C R <sub>L</sub> = 100 kΩ V <sub>O</sub> = ±0.6V R <sub>L</sub> = 10 kΩ	25k		25k	
Supply Current	T <sub>A</sub> = 25°C		8 μA		90 μA
Power Consumption	T <sub>A</sub> = 25°C		24 μW		270 μW
V <sub>OS</sub>	R <sub>S</sub> ≤ 10 kΩ		6.5 mV		7.5 mV
I <sub>OS</sub>			8 nA		25 nA
I <sub>BIAS</sub>			10 nA		80 nA
Input Voltage Range		±0.6V		±0.6V	
Large Signal Voltage Gain	V <sub>O</sub> = ±0.6V R <sub>L</sub> = 100 kΩ R <sub>L</sub> = 10 kΩ	25k		25k	
Output Voltage Swing	R <sub>L</sub> = 100 kΩ R <sub>L</sub> = 10 kΩ	±0.6V		±0.6V	
Common Mode Rejection Ratio	R <sub>S</sub> ≤ 10 kΩ	70 dB		70 dB	
Supply Voltage Rejection Ratio	R <sub>S</sub> ≤ 10 kΩ	74 dB		74 dB	
Supply Current			8 μA		90 μA
Power Consumption			24 μW		270 μW
PARAMETERS	CONDITIONS	V <sub>S</sub> = ±15V			
		I <sub>SET</sub> = 1 μA		I <sub>SET</sub> = 10 μA	
		MIN	MAX	MIN	MAX
V <sub>OS</sub>	T <sub>A</sub> = 25°C R <sub>S</sub> ≤ 100 kΩ		5 mV		6 mV
I <sub>OS</sub>	T <sub>A</sub> = 25°C		6 nA		20 nA
I <sub>BIAS</sub>	T <sub>A</sub> = 25°C		10 nA		75 nA
Large Signal Voltage Gain	T <sub>A</sub> = 25°C R <sub>L</sub> = 100 kΩ V <sub>O</sub> = ±10V R <sub>L</sub> = 10 kΩ	60k		60k	
Supply Current	T <sub>A</sub> = 25°C		11 μA		100 μA
Power Consumption	T <sub>A</sub> = 25°C		330 μW		3 mW
V <sub>OS</sub>	R <sub>S</sub> ≤ 10 kΩ		6.5 mV		7.5 mV
I <sub>OS</sub>			8 nA		25 nA
I <sub>BIAS</sub>			10 nA		80 nA
Input Voltage Range		±13.5V		±13.5V	
Large Signal Voltage Gain	V <sub>O</sub> = ±10V R <sub>L</sub> = 100 kΩ R <sub>L</sub> = 10 kΩ	50k		50k	
Output Voltage Swing	R <sub>L</sub> = 100 kΩ R <sub>L</sub> = 10 kΩ	±12V		±12V	
Common Mode Rejection Ratio	R <sub>S</sub> ≤ 10 kΩ	70 dB		70 dB	
Supply Voltage Rejection Ratio	R <sub>S</sub> ≤ 10 kΩ	74 dB		74 dB	
Supply Current			11 μA		100 μA
Power Consumption			300 μW		3 mW

3

resistor biasing

Set Current Setting Resistor to V<sup>-</sup>

V <sub>S</sub>	I <sub>SET</sub>				
	0.1 μA	0.5 μA	1.0 μA	5 μA	10 μA
±1.5V	25.6 MΩ	5.04 MΩ	2.5 MΩ	492 kΩ	244 kΩ
±3.0V	55.6 MΩ	11.0 MΩ	5.5 MΩ	1.09 MΩ	544 kΩ
±6.0V	116 MΩ	23.0 MΩ	11.5 MΩ	2.29 MΩ	1.14 MΩ
±9.0V	176 MΩ	35.0 MΩ	17.5 MΩ	3.49 MΩ	1.74 MΩ
±12.0V	236 MΩ	47.0 MΩ	23.5 MΩ	4.69 MΩ	2.34 MΩ
±15.0V	296 MΩ	59.0 MΩ	29.5 MΩ	5.89 MΩ	2.94 MΩ

ข้อมูลทางไฟฟ้าของทรานซิสเตอร์

**2N2904, A thru 2N2907, A (SILICON)**  
**2N3485, A, 2N3486, A**

**PNP SILICON ANNULAR HERMETIC TRANSISTORS**

... designed for high-speed switching circuits, DC to VHF amplifier applications and complementary circuitry.

- High DC Current Gain Specified – 0.1 to 500 mAdc
- High Current-Gain–Bandwidth Product –  
 $f_T = 200 \text{ MHz (Min) @ } I_C = 50 \text{ mAdc}$
- Low Collector-Emitter Saturation Voltage –  
 $V_{CE(sat)} = 0.4 \text{ Vdc (Max) @ } I_C = 150 \text{ mAdc}$
- 2N2904,A thru 2N2907,A Complement to NPN 2N2218,A,  
 2N2219,A, 2N2221,A, 2N2222,A
- JAN/JTX Available, Except 2N3485 and 2N3486.

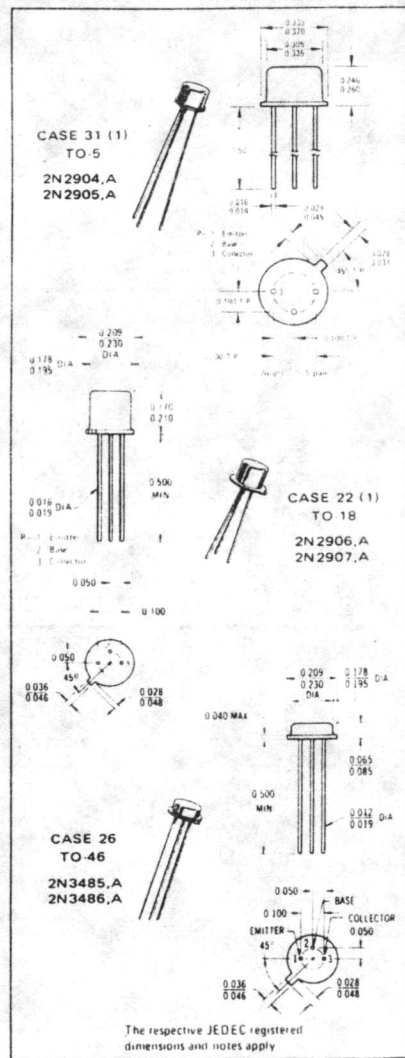
**PNP SILICON SWITCHING AND AMPLIFIER TRANSISTORS**

**SELECTOR GUIDE**

Device Type	Characteristic				Package
	BV <sub>CEO</sub> I <sub>C</sub> = 10 mAdc Volts	I <sub>C</sub> = 1.0 mAdc Min	h <sub>FE</sub> I <sub>C</sub> = 150 mAdc Min	I <sub>C</sub> = 500 mAdc Min	
2N2904 2N2905	40	25	40	20	TO-5
2N2906 2N2907		50	100	30	
2N3485 2N3486		25	40	20	
		50	100	30	
2N2904A 2N2905A	60	40	40	40	TO-5
2N2906A 2N2907A		100	100	50	
2N3485A 2N3486A		40	40	40	
		100	100	50	

**\*MAXIMUM RATINGS**

Rating	Symbol	Non-A Suffix	A-Suffix	Unit	
Collector-Emitter Voltage	V <sub>CEO</sub>	40	60	Vdc	
Collector-Base Voltage	V <sub>CB</sub>		60	Vdc	
Emitter-Base Voltage	V <sub>EB</sub>		5.0	Vdc	
Collector Current – Continuous	I <sub>C</sub>		600	mAdc	
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	2N2904,A 2N2905,A	2N2906,A 2N2907,A	2N3485,A 2N3486,A	600 400 400 mW
		3.43	2.28	2.28	mW/°C
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>				3.0 1.8 2.0 Watts
		17.2	10.3	11.43	mW/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +200			°C



The respective JEDEC registered dimensions and notes apply

## ภาคผนวก ข. (ต่อ)

## 2N2904,A thru 2N2907,A/2N3485,A, 2N3486,A (continued)

\*ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ$  unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit	
<b>OFF CHARACTERISTICS</b>						
Collector-Emitter Breakdown Voltage(1) ( $I_C = 10 \text{ mAdc}$ , $I_B = 0$ )	Non-A Suffix A-Suffix $BV_{CEO}$	40 60	—	—	Vdc	
Collector-Base Breakdown Voltage ( $I_C = 10 \mu\text{Adc}$ , $I_E = 0$ )	$BV_{CBO}$	60	—	—	Vdc	
Emitter-Base Breakdown Voltage ( $I_E = 10 \mu\text{Adc}$ , $I_C = 0$ )	$BV_{EBO}$	5.0	—	—	Vdc	
Collector Cutoff Current ( $V_{CE} = 30 \text{ Vdc}$ , $V_{BE} = 0.5 \text{ Vdc}$ )	$I_{CEX}$	—	—	50	nAdc	
Collector Cutoff Current ( $V_{CB} = 50 \text{ Vdc}$ , $I_E = 0$ )	Non-A Suffix A-Suffix $I_{CBO}$	— —	— —	0.020 0.010	$\mu\text{Adc}$	
( $V_{CB} = 50 \text{ Vdc}$ , $I_E = 0$ , $T_A = 150^\circ\text{C}$ )	Non-A Suffix A-Suffix	— —	— —	20 10		
Base Cutoff Current ( $V_{CE} = 30 \text{ Vdc}$ , $V_{BE} = 0.5 \text{ Vdc}$ )	$I_B$	—	—	50	nAdc	
<b>ON CHARACTERISTICS</b>						
DC Current Gain ( $I_C = 0.1 \text{ mAdc}$ , $V_{CE} = 10 \text{ Vdc}$ )	2N2904,2N2906,2N3485 2N2905,2N2907,2N3486 2N2904A,2N2906A,2N3485A 2N2905A,2N2907A,2N3486A	$h_{FE}$	20 35 40 75	— — — —	— — — —	
( $I_C = 1.0 \text{ mAdc}$ , $V_{CE} = 10 \text{ Vdc}$ )	2N2904,2N2906,2N3485 2N2905,2N2907,2N3486 2N2904A,2N2906A,2N3485A 2N2905A,2N2907A,2N3486A		25 50 40 100	— — — —	— — — —	
( $I_C = 10 \text{ mAdc}$ , $V_{CE} = 10 \text{ Vdc}$ )	2N2904,2N2906,2N3485 2N2905,2N2907,2N3486 2N2904A,2N2906A,2N3485A 2N2905A,2N2907A,2N3486A		35 75 40 100	— — — —	— — — —	
( $I_C = 150 \text{ mAdc}$ , $V_{CE} = 10 \text{ Vdc}$ )(1)	2N2904,A,2N2906,A,2N3485,A 2N2905,A,2N2907,A,2N3486,A		40 100	— —	120 300	
( $I_C = 500 \text{ mAdc}$ , $V_{CE} = 10 \text{ Vdc}$ )(1)	2N2904,2N2906,2N3485 2N2905,2N2907,2N3486 2N2904A,2N2906A,2N3485A 2N2905A,2N2907A,2N3486A		20 30 40 50	— — — —	— — — —	
Collector-Emitter Saturation Voltage(1) ( $I_C = 150 \text{ mAdc}$ , $I_B = 15 \text{ mAdc}$ ) ( $I_C = 500 \text{ mAdc}$ , $I_B = 50 \text{ mAdc}$ )	$V_{CE(sat)}$	— —	— —	0.4 1.6	Vdc	
Base-Emitter Saturation Voltage ( $I_C = 150 \text{ mAdc}$ , $I_B = 15 \text{ mAdc}$ )(1) ( $I_C = 500 \text{ mAdc}$ , $I_B = 50 \text{ mAdc}$ )	$V_{BE(sat)}$	— —	— —	1.3 2.6	Vdc	
<b>DYNAMIC CHARACTERISTICS</b>						
Current-Gain-Bandwidth Product(2) ( $I_C = 50 \text{ mAdc}$ , $V_{CE} = 20 \text{ Vdc}$ , $f = 100 \text{ MHz}$ )	$f_T$	200	—	—	MHz	
Output Capacitance ( $V_{CB} = 10 \text{ Vdc}$ , $I_E = 0$ , $f = 100 \text{ kHz}$ )	$C_{ob}$	—	—	8.0	pF	
Input Capacitance ( $V_{BE} = 2.0 \text{ Vdc}$ , $I_C = 0$ , $f = 100 \text{ kHz}$ )	$C_{ib}$	—	—	30	pF	
<b>SWITCHING CHARACTERISTICS</b>						
Turn-On Time	$(V_{CC} = 30 \text{ Vdc}$ , $I_C = 150 \text{ mAdc}$ , $I_{B1} = 15 \text{ mAdc}$ ) (Figure 15a)	$t_{on}$	—	26	45	ns
Delay Time		$t_d$	—	6.0	10	ns
Rise Time		$t_r$	—	20	40	ns
Turn-Off Time	$(V_{CC} = 6.0 \text{ Vdc}$ , $I_C = 150 \text{ mAdc}$ , $I_{B1} = I_{B2} = 15 \text{ mAdc}$ ) (Figure 15b)	$t_{off}$	—	70	100	ns
Storage Time		$t_s$	—	50	80	ns
Fall Time		$t_f$	—	20	30	ns

\*Indicates JEDEC Registered Data.

(1) Pulse Test: Pulse Width  $\leq 300 \mu\text{s}$ , Duty Cycle  $\leq 2.0\%$ .(2)  $f_T$  is defined as the frequency at which  $|h_{fe}|$  extrapolates to unity.



2N2904,A thru 2N2907,A/2N3485,A, 2N3486,A (continued)

FIGURE 1 - NORMALIZED DC CURRENT GAIN

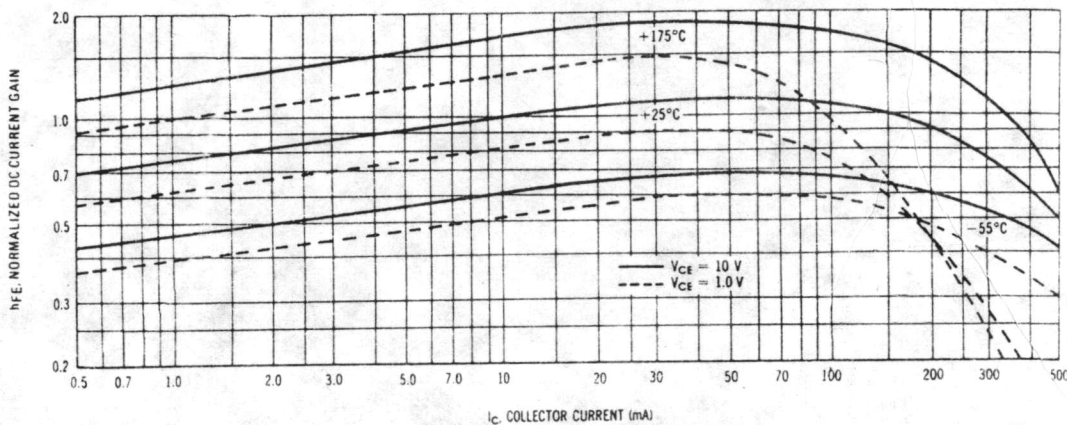
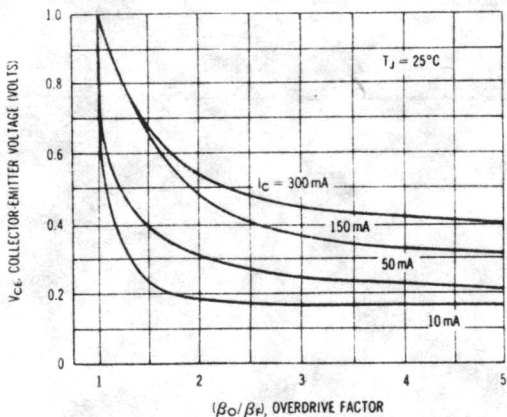


FIGURE 2 - NORMALIZED COLLECTOR SATURATION REGION



This graph shows the effect of base current on collector current.  $\beta_0$  (current gain at edge of saturation) is the current gain of the transistor at 1 volt, and  $\beta_s$  (forced gain) is the ratio of  $I_C/I_{BF}$  in a circuit.

EXAMPLE: For type 2N2905, estimate a base current ( $I_{BF}$ ) to insure saturation at a temperature of  $25^\circ\text{C}$  and a collector current of 150 mA.

Observe that at  $I_C = 150\text{ mA}$  an overdrive factor of at least 3 is required to drive the transistor well into the saturation region. From Figure 1, it is seen that  $h_{FE}$  @ 1 volt is approximately 0.60 of  $h_{FE}$  @ 10 volts. Using the guaranteed minimum of 100 @ 150 mA and 10 V,  $\beta_0 = 60$  and substituting values in the overdrive equation, we find:

$$\frac{\beta_0}{\beta_s} = \frac{h_{FE} @ 1\text{ V}}{I_C/I_{BF}} \quad 3 = \frac{60}{150/I_{BF}} \quad I_{BF} \approx 7.5\text{ mA}$$

FIGURE 3 - "ON" VOLTAGES

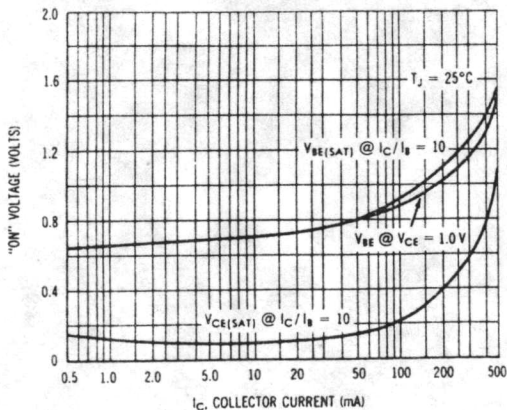
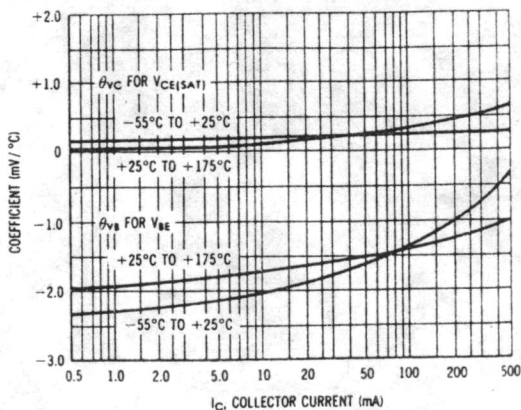


FIGURE 4 - TEMPERATURE COEFFICIENTS

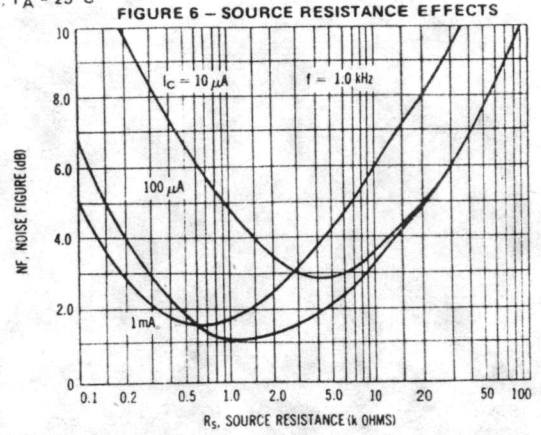
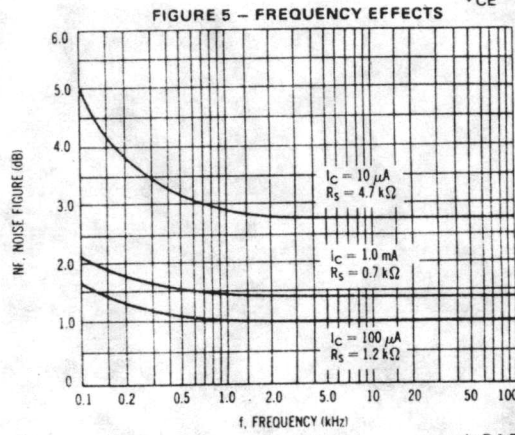


ภาคผนวก ข. (ต่อ)

2N2904,A thru 2N2907,A/2N3485,A, 2N3486,A (continued)

SMALL-SIGNAL CHARACTERISTICS  
NOISE FIGURE

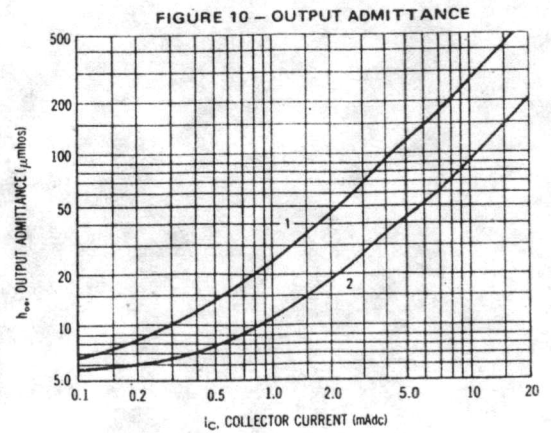
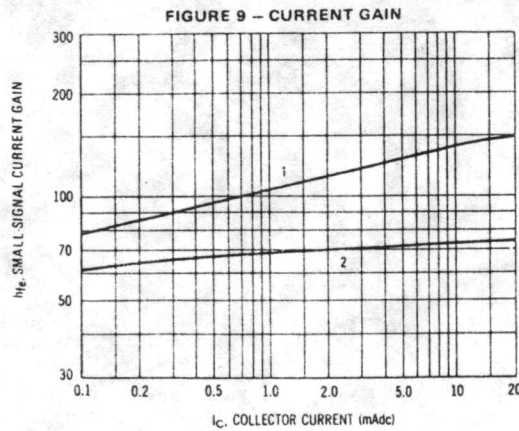
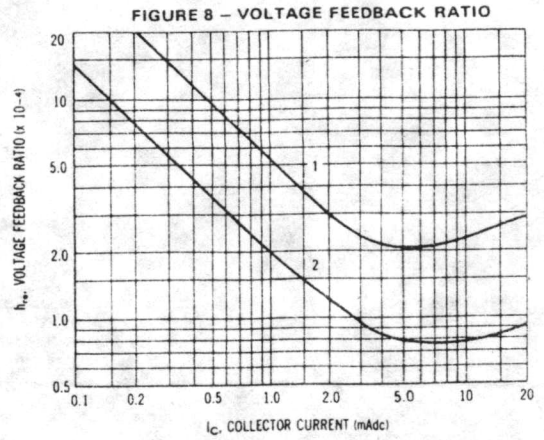
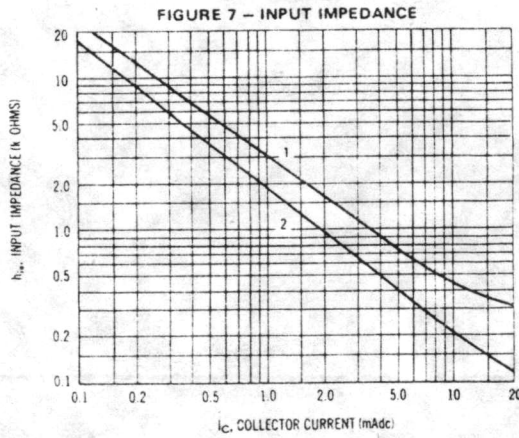
VCE = 10 V, TA = 25°C



h PARAMETERS

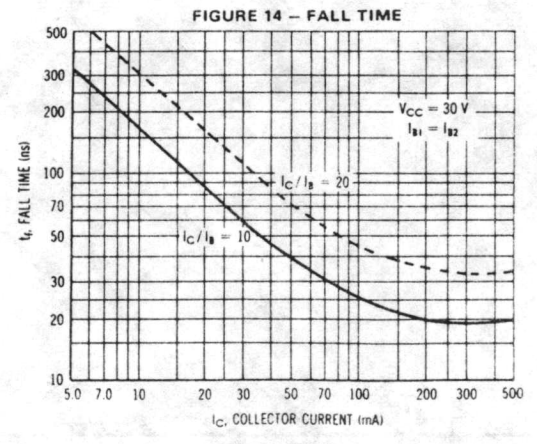
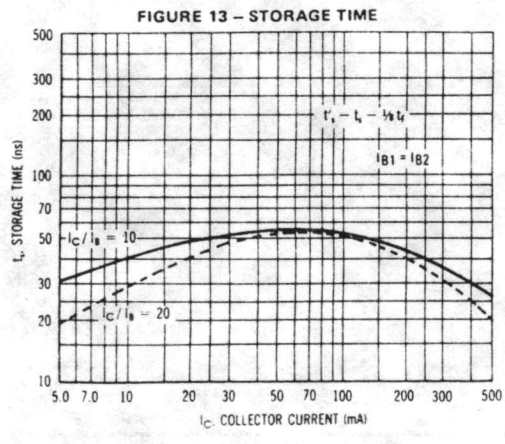
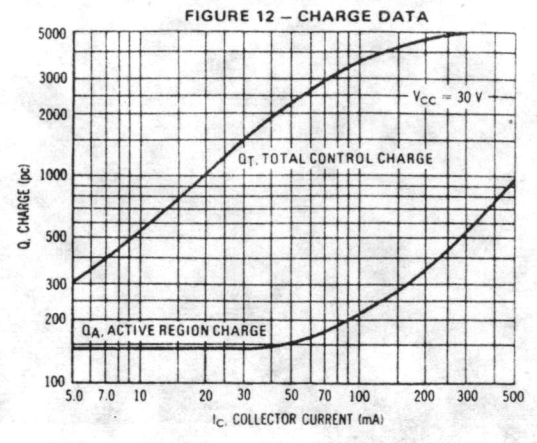
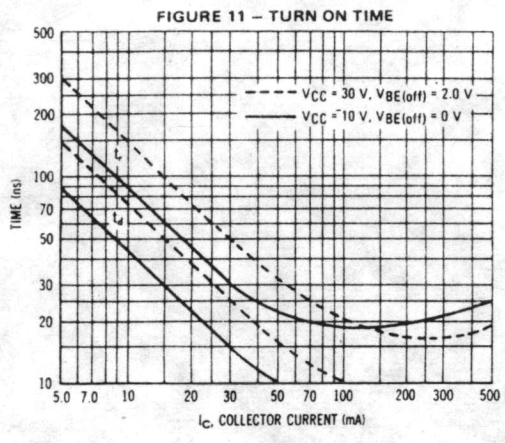
VCE = 10 Vdc, f = 1.0 kHz, TA = 25°C

This group of graphs illustrates the relationship between  $h_{fe}$  and other "h" parameters for this series of transistors. To obtain these curves, a high-gain and a low-gain unit were selected and the same units were used to develop the correspondingly numbered curves on each graph.

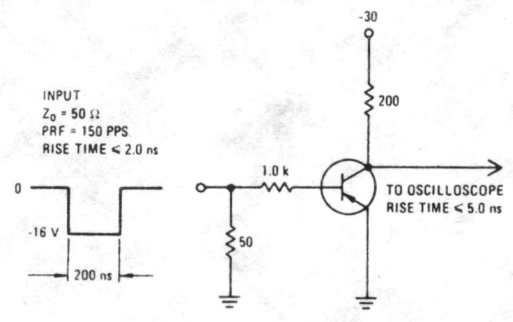


ภาคผนวก ข. (ต่อ)

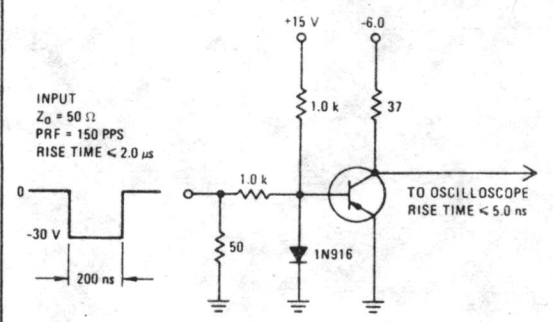
2N2904,A thru 2N2907,A/2N3485,A, 2N3486,A (continued)



**FIGURE 15a - DELAY AND RISE TIME TEST CIRCUIT**

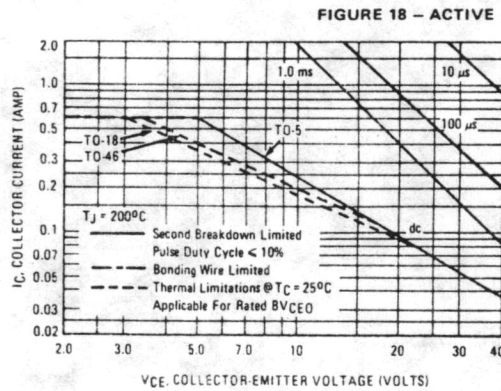
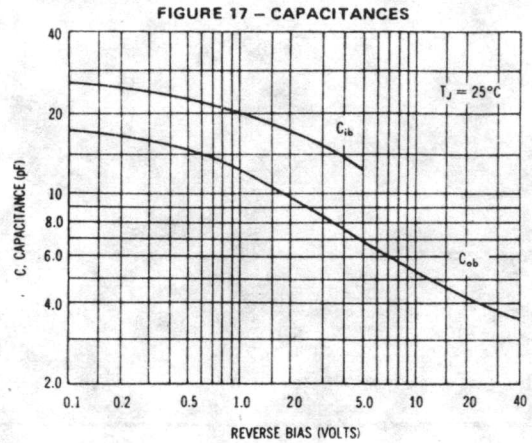
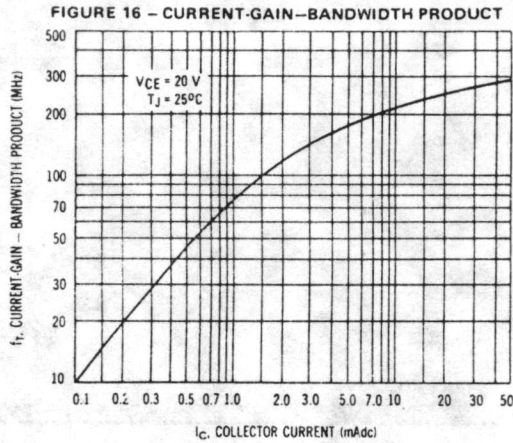


**FIGURE 15b - STORAGE AND FALL TIME TEST CIRCUIT**



ภาคผนวก ข. (ต่อ)

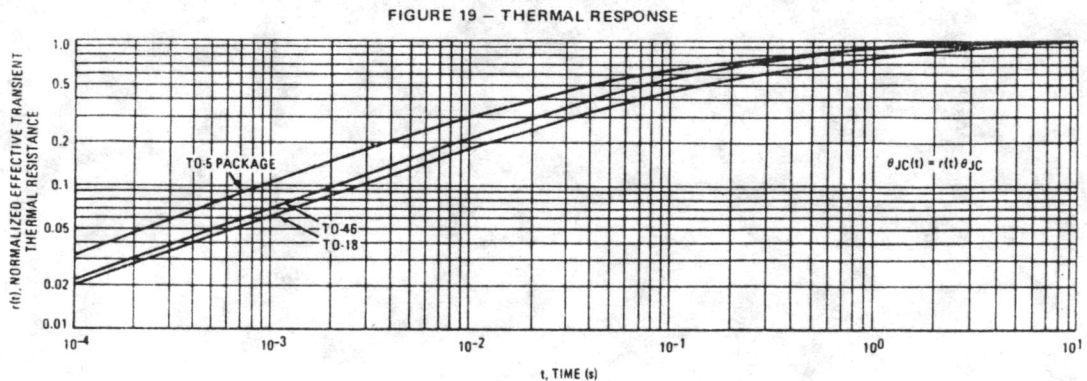
2N2904,A thru 2N2907,A/2N3485,A, 2N3486,A (continued)



This graph shows the maximum  $I_C$ - $V_{CE}$  limits of the device both from the standpoint of thermal dissipation (at  $25^\circ\text{C}$  case temperature), and secondary breakdown. For case temperatures other than  $25^\circ\text{C}$ , the thermal dissipation curve must be modified in accordance with the derating factor in the Maximum Ratings table.

To avoid possible device failure, the collector load line must fall below the limits indicated by the applicable curve. Thus, for certain operating conditions the device is thermally limited, and for others it is limited by secondary breakdown.

For pulse applications, the maximum  $I_C$ - $V_{CE}$  product indicated by the dc thermal limits can be exceeded. Pulse thermal limits may be calculated by using the transient thermal resistance curve of Figure 19.



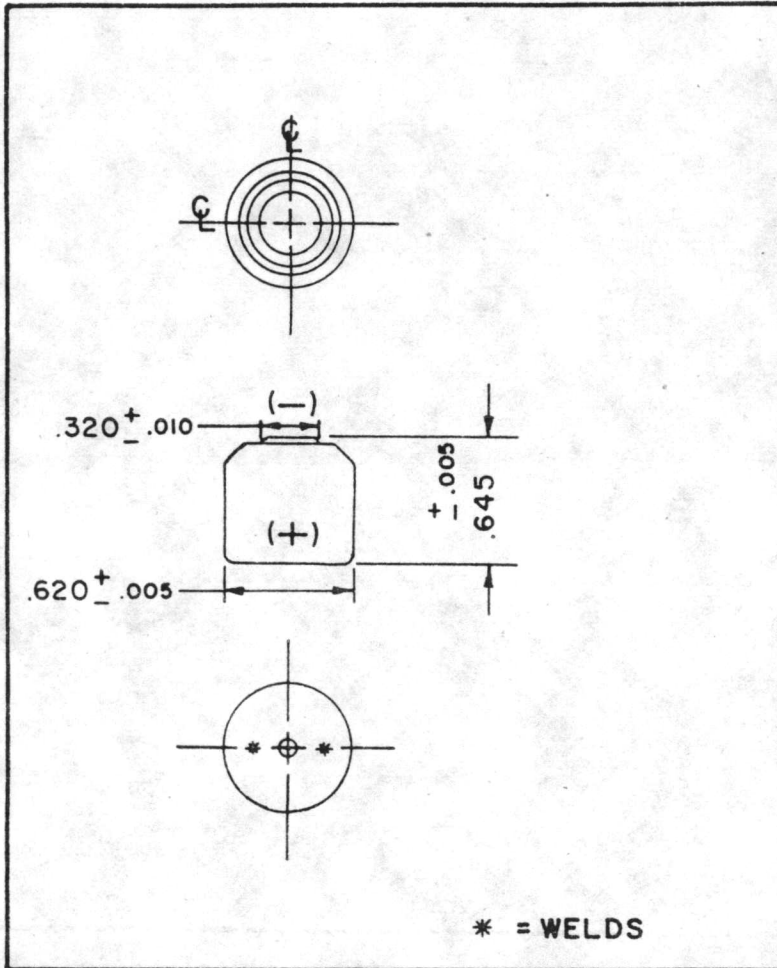
**MALLORY**

**MALLORY BATTERY COMPANY**

a division of P. R. MALLORY & CO. INC.  
S. Broadway, Tarrytown, New York, 10591; Telephone: 914-691-7000



**CERTIFIED CELL** **317937**



INCHES DEC.	M.M.
.005	.13
.010	.25
.320	8.13
.620	15.75
.645	16.38

\* = WELDS

**SPECIFICATIONS**

TYPE \_\_\_\_\_ MERCURIC OXIDE SILVER  
 NOMINAL VOLTAGE \_\_\_\_\_ 1.38 ± .03 VOLTS  
 SHORT CIRCUIT CURRENT \_\_\_\_\_ 100 MA. MIN.  
 SERVICE CAPACITY (AT 98.6° F TO 0.9 VOLTS) 1,000 MAH  
 (RATED CAPACITY AT 1330 ± 1% OHMS)  
 AVERAGE WEIGHT \_\_\_\_\_ 13.6 GMS (.48 OZ)  
 VOLUME \_\_\_\_\_ 3.28 C.C. (.20 CU. IN.)  
 WELDED INNER & OUTER CANS



ภาคผนวก ค. (ต่อ)

CELL TYPE — 317937

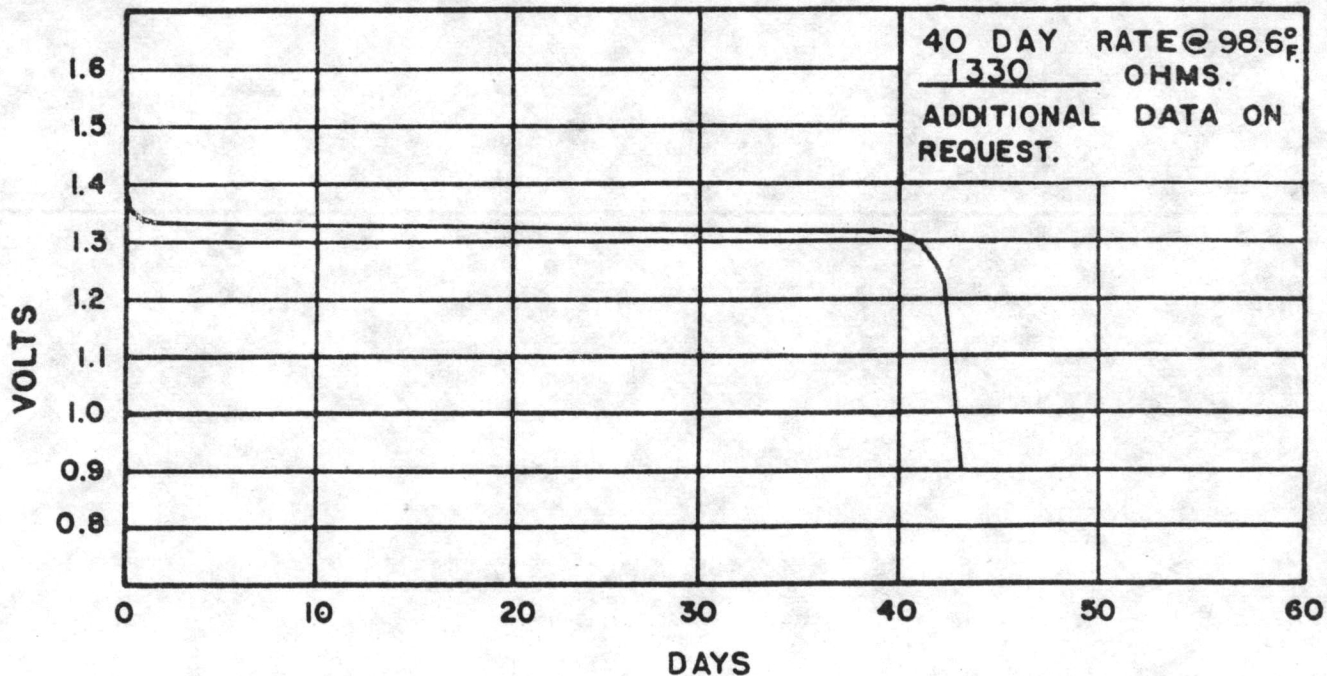
RATED CAPACITY — 1000 MA

**DISCHARGE DATA—**

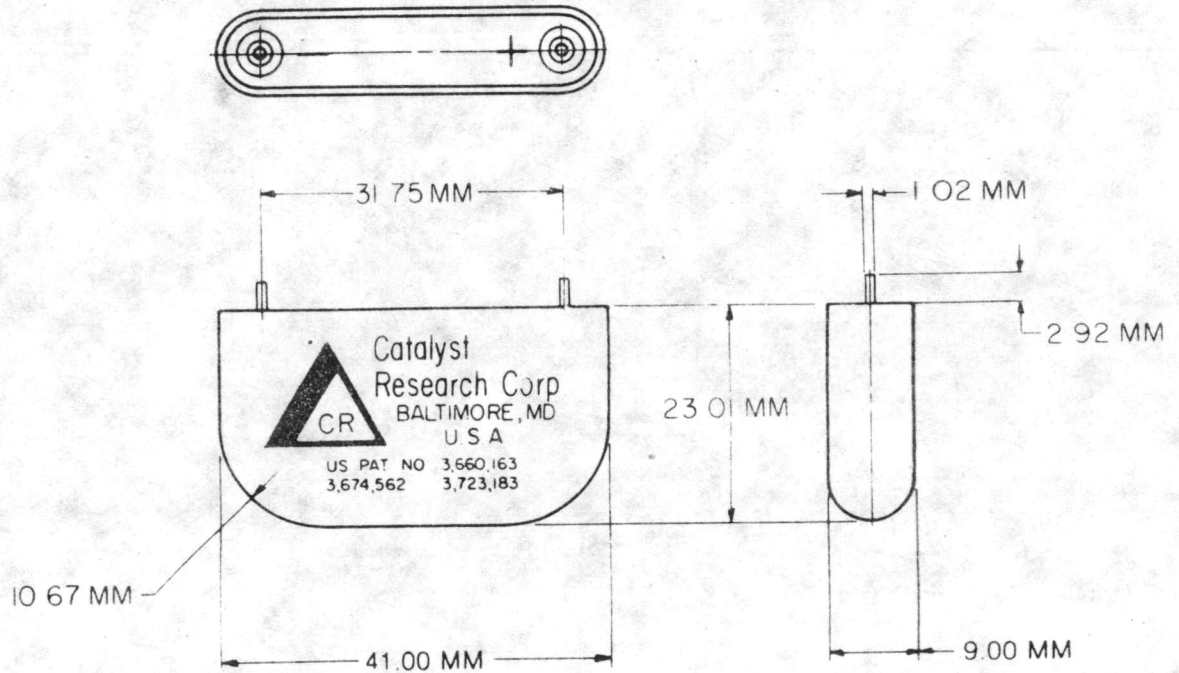
<u>DUTY CYCLE</u>	<u>STARTING DRAIN</u> <u>IN MILLIAMPERES</u>	<u>LOAD</u>	<u>TIME TO</u> <u>END-POINT VOLTAGE</u>
CONTINUOUS 24 HRS/DAY AT 98.6° F.	1.0	1330 <sup>+1%</sup> OHMS	<u>.9</u> 40 DAYS MIN.

THIS CELL IS DESIGNED FOR HIGH RELIABILITY MEDICAL APPLICATIONS REQUIRING EXTENDED SERVICE AT MICROAMPERE CURRENTS AND BODY TEMPERATURES. FOR DETAILED DISCHARGE INFORMATION CONTACT MALLORY BATTERY CO.

**TYPICAL DISCHARGE (VOLTAGE VS. TIME)**



### Model 804B/23 SOLID LITHIUM IODINE CELL



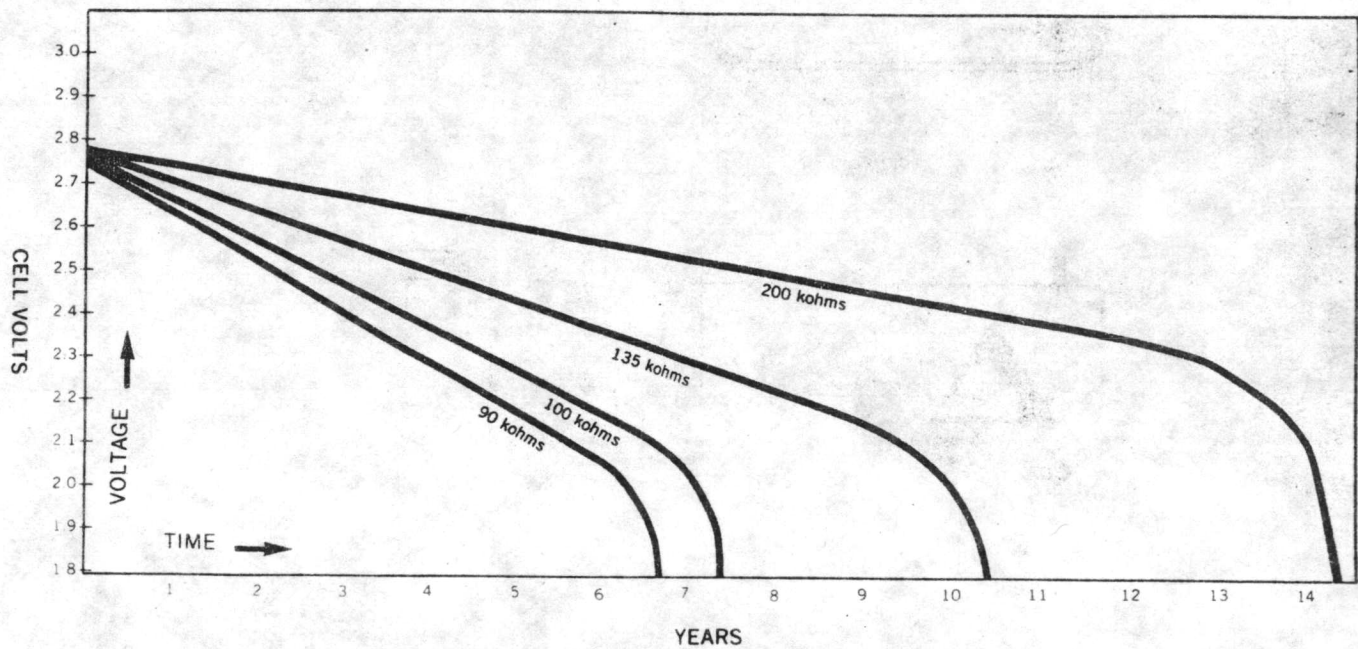
Volume	7.34 cm <sup>3</sup>
Weight	20.5 grams
Lithium Area	14.22 cm <sup>2</sup>
Voltage	2.80 volts under no load
Recommended Currents	1 to 50 microamperes at 37°C
Nominal Capacity	1.7 ampere-hours
Energy	4.1 watt-hours
Energy Density	0.56 watt-hours/cm <sup>3</sup> 0.20 watt-hours/gram
Self-Discharge	Less than 5% in 10 years
Seal	Heliarc welded with glass/metal hermetic seals. Less than 4.6 x 10 <sup>8</sup> max helium leak, by helium back fill method
Insulation Resistance	Greater than 10 <sup>10</sup> ohms from either pin to case
Storage Temp	-40°C to 50°C with brief excursions to 60°C

The capacities shown are based upon average fill weights. Standard deviation of capacity from battery to battery is approximately .03 ampere-hours.

### Model 804B/23 SOLID LITHIUM IODINE CELL



#### PROJECTED PERFORMANCE CONSTANT CURRENT DISCHARGE



GROSS CAPACITY ABOVE 2.0 VOLTS

LOAD (in kohms)	50	90	100	135	200
CAPACITY (in amp. hrs.)	1.35	1.57	1.60	1.67	1.71

#### PROJECTED PERFORMANCE CONSTANT CURRENT DISCHARGE

CONSTANT CURRENT = 15 MICROAMPS

AMP HRS EXPENDED	TIME YEARS	CELL VOLTS	IMPEDANCE KOHMS
00	00	2.79	39
13	83	2.75	3.01
26	1.79	2.71	6.01
40	2.74	2.66	9.00
53	3.69	2.62	12.00
66	4.64	2.57	15.01
78	5.59	2.53	18.01
91	6.54	2.48	21.00
1.04	7.49	2.44	24.01
1.17	8.43	2.39	27.00
1.30	9.38	2.35	30.01
1.43	10.32	2.30	33.01
1.55	11.26	2.26	36.01
1.65	11.96	2.21	39.01
1.69	12.27	2.17	42.07

CONSTANT CURRENT = 25 MICROAMPS

AMP. HRS. EXPENDED	TIME YEARS	CELL VOLTS	IMPEDANCE KOHMS
00	00	2.79	40
15	62	2.72	3.01
32	1.33	2.65	6.02
48	2.04	2.57	9.01
64	2.74	2.50	12.02
79	3.45	2.42	15.02
95	4.15	2.35	18.01
1.11	4.85	2.27	21.02
1.27	5.55	2.20	24.01
1.42	6.26	2.12	27.02
1.58	6.94	2.05	30.02
1.66	7.33	1.97	33.02
1.70	7.47	1.90	36.00
1.71	7.54	1.82	39.27

CONSTANT CURRENT = 30 MICROAMPS

AMP. HRS. EXPENDED	TIME YEARS	CELL VOLTS	IMPEDANCE KOHMS
00	00	2.79	40
16	.56	2.71	3.02
34	1.20	2.62	6.02
51	1.83	2.53	9.02
68	2.46	2.44	12.00
85	3.10	2.35	15.01
1.02	3.73	2.26	18.01
1.19	4.36	2.17	21.02
1.36	4.99	2.08	24.02
1.53	5.62	1.99	27.01
1.65	6.07	1.90	30.04
1.69	6.23	1.81	33.14
1.71	6.29	1.71	36.21
1.72	6.33	1.62	39.35
1.72	6.35	1.53	42.18







## ประวัติผู้เขียน

นายครรชิต โขมพัตร เกิดเมื่อวันที่ ๓๑ พฤษภาคม พ.ศ. ๒๔๘๗ ณ จังหวัดเชียงใหม่ จบการศึกษาระดับมัธยมศึกษาตอนปลายชั้นปีที่ ๕ จากโรงเรียน ภ.ป.ร.ราชวิทยาลัย สามพราน นครปฐม สำเร็จการศึกษาระดับปริญญาตรี สาขาวิศวกรรมไฟฟ้า (อิเล็กทรอนิกส์) จากคณะวิศวกรรมศาสตร์ สถาบันเทคโนโลยีพระจอมเกล้าวิทยาเขตธนบุรี เมื่อ พ.ศ. ๒๕๑๔ เคยเป็นอาจารย์ผู้ช่วยที่คณะวิศวกรรมไฟฟ้า จุฬาลงกรณ์มหาวิทยาลัยเมื่อ พ.ศ. ๒๕๒๐ ได้เสนอบทความทางวิชาการในการประชุมทางวิชาการวิศวกรรมไฟฟ้า ครั้งที่ ๔ ที่สถาบันเทคโนโลยีพระจอมเกล้า วิทยาเขตเจ้าคุณทหาร ลาดกระบัง เมื่อ พ.ศ. ๒๕๒๔

ปัจจุบันรับราชการตำแหน่งวิศวกร ๔ ประจำหน่วยซ่อมสร้างเครื่องอิเล็กทรอนิกส์ ภาควิชา สรีรวิทยา คณะแพทยศาสตร์ศิริราชพยาบาล มหาวิทยาลัยมหิดล