REFERENCES

- [1] S. Lin, C. Lursinsap, and D. Gajski, "Silicon Cell Complier" Advances in Computer-Aided Engineering Design, Vol 2. edited by I. Hajj, published by JAI Press, Chapter 1, pp. 1-55.
- [2] V. Raghavendra and C. Lursinsap, "A Technique for Micro-Rollback Self-Recovery Synthesis", IEEE Transactions on Computer-Aided Design, Vol.14, No.9, Sept 1995, pp.1171-1179.
- [3] R. Karri and A. Orailoglu, "Scheduling with Rollback Constraints in High-Level

 Synthesis of Self-Recovering ASICs", IEEE Workshops on Fault-Tolerant

 Parallel and Distributed Systems, 19992, pp.519-526.
- [4] P. G. Paulin and J. P. Knight, "Force-Directed Scheduling for the Behavioral Synthesis of ASIC's", IEEE Transactions on Computer-Aided Design, Vol.8, No.6, June 1989, pp.661-679.
- [5] A. Ziv and J. Bruck, "An On-Line Algorithm for Checkpoint Placement", IEEE Transactions on Computers, Vol.46, No.9, Sept 1997, pp.976-985.
- [6] Y. Tamir and M. Tremblay, "High-Performance Faut-Tolerant VLSI Systems

 Using Micro Rollback", IEEE Transactions on Computers, Vol.39, No.4, Apr

 1990, pp.548-554.
- [7] G. W. Grewal and T. C. Wilson, "An Enhanced Genetic Solution, Module Allocation, and Binding in VLS1 Design", Proceeding of 10th IEEE Inter. Conf. on VLS1 Design, Jan 1997, pp.51-56.
- [8] M.J.M. Heijligers, L.J.M. Cluitmans and J.A.G.Jess, "High Level Synthesis

 Scheduling and Allocation using Genetic Algorithms", IEEE transaction on

 Design Automation Conference 1995.
- [9] B. Iyer, R. Karri and I. Koren, "Phantom Redundancy: A High-Level Synthesis

 Approach for Manufacturability", IEEE/ACM Inter.Conf. on Computer-Aided

 Design, 1995,pp.368-361.

- [10] A. Orailoglu and R. Karri, "Coactive Scheduling and Checkpoint Determination

 During High Level Synthesis of Self-Recovering Micro architectures", IEEE

 Transaction on VLSI System, Vol.2, No.3, Sept 1994, pp.304-311.
- [11] D. M. Blough, F. J. Kurdahi and S. Y. Ohm, "Optimal Recovery Point Insertion for High-Level Synthesis of Recoverable Micro architectures", Proceeding of 25th IEEE Inter. Sym. on Fault-Tolerant Computing, 1995, pp.50-59.
- [12] S. W. Smith, D. B. Johnson and J. D. Tyger, "Completely Asynchronous Optimistic Recovery with Minimal Rollbacks", Proceeding of 25th IEEE Inter. Sym. on Fault-Tolerant Computing, 1995, pp.361-370.
- [13] D. E. Goldberg, Genetic Algorithms in Search, optimization, and Machine Learning, Addison-Wesley, Reading, Massachusetts, 1989.
- [14] I. Ahmad, M. Dhodhi, and C. Chen, "Integrated Scheduling, Allocation and Module Selection for Design-Space Exploration in High-Level Synthesis", IEEE Proc.-Comput. Digit. Tech., Vol.142, No.1, January 1995, pp. 65-71.
- [15] Z. Michalewicz, Genetic Algorithms + Data Structures = Evolution Programs,
 Springer, 1995.
- [16] L. Davis, *Handbook of Genetic Algorithms*, Van Nostrand Reinhold New York, 1991.
- [17] K. F. Man, K. S. Tang, and S. Kwong, "Genetic Algorithms: Concepts and Applications", IEEE Transaction on Industrial Electronic, Vol.43, No.5, Oct 1996.
- [18] K. Sookhanaphibarn and C. Lursinsap, "Genetic Self-Recovery Micro-Rollback Synthesis", Proceeding of IEEE Congress on Evolutionary Computation, July 6-9, 1999.

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Publications

- K. Sookhanaphibarn and C. Lursinsap, "Genetic Self-Recovery Micro-Rollback Synthesis", IEEE 1999 Congress on Evolutionary Computation (CEC99), Washington D.C., USA, 1999
- 2. K. Sookhanaphibarn, T. Kitcharoensup, J.Wuttichaivaragul, and C. Lursinsap, "Recognition of Naked Bodies by Supervised Neural Network", The National Computer Science and Engineering Conference (NCSEC'98), Thailand, 1998
- 3. K. Sookhanaphibarn and C. Lursinsap, "Applying Genetic Algorithm to High-Level Synthesis", The Second Annual National Symposium on computational Science and Engineering (ANSCSE'98), Thailand, 1998